

Total No. of Questions : 8] [ Total No. of Printed Pages : 4

**Paper Code : 21312**

**F-412**

**B.C.A. (Third Semester)**

**Examination, 2021**

**(New Course)**

**Paper - BCA-302**

**COMPUTER ORGANIZATION**

**Time : Three Hours ] [ Maximum Marks : 70**

**Note : Attempt any five questions. All questions carry equal marks.**

1. (a) Explain the concept of RISC and CISC
- (b) What is set associative memory? Explain with the help of a block diagram. In which situation set associative memory can be effectively utilized.

(1)

P.T.O.

2. (a) Explain in brief :
  - (i) Address Bus
  - (ii) Main Memory
  - (iii) Cache Memory
- (b) What are single precision and double precision format. Express 42.687 in IEEE Single Precision point representation.
3. (a) Describe Bus Arbitration. Explain four different types of bus arbitration.
- (b) What are the concepts of input-output interfacing and I/O processor.
4. (a) What is the concept of Daisy Chaining. Explain DMA in detail.
- (b) Discuss Cache Memories. What is High speed cache? Explain cache Hit and cache Miss.

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(2)

5. (a) Draw a block diagram to illustrate the basic organization of a computer System and explain the function of the logic units.

(b) Explain generation of Computers with their characteristics.

6. (a) Describe the concept of Paging and Page Replacement algorithm.

(b) Consider this page reference stream  
12321521625631361243.

Page frame size is 3. Calculate page faults using First In First Out. Optimal Replacement, Least Recently Used.

7. (a) Write down Booth's Algorithm with the help of example.

(b) What are different types of Interrupts describe any one with examples.

8. Write short notes on any **four** of the following:

(a) Processor organization?

(b) Primary and Secondary memory.

(c) Direct and Indirect addressing.

(d) Thrashing.

(e) Multi programming.

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